

Slicing and Scope-Bounded Verification with Polymorphic Region and Effect Inference

Mikhail Mandrykin

ISP RAS

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Issues

Target programs

Large programs, obtained from industrial software systems, $\gtrsim 10\text{KLoc}$

Target issues

- Relatively large proportion of **irrelevant code**, typically simple abstractions suffice to efficiently prune the search space
- Analysis is typically **global** i.e. function calls are **inlined** at least once (for BAM)
- Incomplete code prompts issues with pointer analysis, especially **assignments through unknown pointers** that in the worst case can invalidate the entire abstract state

Overview

Suggested approach

Employ a **fast**, **local** and **sound** separation analysis, even if it is a relatively crude (imprecise) one

Solutions

- **Slicing** based on effect inference and interference
- **Scope-bounded verification** by skipping function calls soundly approximated with non-deterministic effects
- Avoid complete loss of precision by **strengthening** the global analyses with the results of the preliminary local separation analysis

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Core language

$t_n ::=$	numeric term
v	numeric variable
$a(t_n, \dots, t_n)$	arithmetic expression on numeric terms
$*$	non-deterministic value
$t_{p_1} - t_{p_2}$	pointer difference
$t_p \rightarrow f_n$	numeric field access
$t_p ::=$	pointer term
p	pointer variable
NULL	NULL pointer
$*$	start of a fresh memory block
$t_p + t_n$	pointer shift
$t_p \rightarrow f_p$	pointer field access
$\&t_p \rightarrow f_s$	nested structure
$\text{container_of}(t_p, f_s)$	outer structure (field names are globally unique) (struct outer *)((char *)inner - offsetof(outer, inner))
$i ::=$	instruction
$t_n = t_n$	numeric assignment
$t_p = t_p$	pointer assignment
$\text{assert}(a(t_n, \dots, t_n))$	assertion
$\text{assume}(a(t_n, \dots, t_n))$	assumption
$f(t_p, \dots, t_p, t_n, \dots, t_n)$	function call

Reduction (operators)

- Eliminate occurrences of the **lone address operator** & (i.e. without the arrow \rightarrow)

```
struct s { int f; } s;           ⇒      struct s { int f; } *s =  
int v;                           alloca(sizeof(struct s));  
int *p = &v, u = v, *w = &s.f;     int *v = alloca(sizeof(int));  
                                int *p = v, u = *v, w = &s->f;
```

- Eliminate occurrences of the **dereference operator** *

```
int *p;                         ⇒      struct int_ { int f };  
int v = *p;                      struct int_ *p;  
                                int v = p->f;
```

- Eliminate occurrences of the **dot operator** .

```
struct s { struct int_ f; } s; ⇒      struct s { struct int_ f; } *s =  
int v = s.f.f;                   alloca(sizeof(strut s));  
                                int v = (&s->f)->f;
```

Reduction

Sample program (before)

```
1 struct pair { int a, b; };
2 struct s { struct pair *p;
3             int v };
4 void swap(struct s **x,
5           struct s **y)
6 {
7     struct s *t = *x;
8     *x = *y;
9     *y = t;
10    struct pair *p = t->p;
11    t->p = (*x)->p;
12    (*x)->p = p;
13 }
```

```
14 void caller()
15 {
16     struct s x, y, z;
17     struct s *px = &x,
18             *py = &y,
19             *pz = &z;
20     // ...
21     swap(&px, &py);
22     swap(&py, &pz);
23 }
```

Reduction

Sample program (after)

```
1 struct pair { int a, b; };
2 struct s { struct pair *p;
3             int v };
4 struct sP { struct s* sM; };
5 void swap(struct sP *x,
6           struct sP *y)
7 {
8     struct s *t = x->sM;
9     x->sM = y->sM;
10    y->sM = t;
11    struct pair *p = t->p;
12    t->p = x->sM->p;
13    x->sM->p = p;
14 }
```

```
15 void caller()
16 {
17     struct s *x = alloca(...),
18         *y = alloca(...),
19         *z = alloca(...);
20     struct sP *px = alloca(...),
21         *py = alloca(...),
22         *pz = alloca(...);
23     px->sM = x;
24     py->sM = y;
25     pz->sM = z;
26     // ...
27     swap(px, py);
28     swap(py, pz);
29 }
```

Reduction (unions and pointer type casts)

- Introduce **empty structure** with tag `void_`.
- Introduce **prefix field** `void_` of type `struct void_` to every structure, except `void_`.
- Wrap union fields into structures (as with `int` → `struct int`)
- Translate non-prefix casts:

```
struct int_ *pn;
struct char_ *pc = (struct char_ *)pn;
                ↓
struct int_ *pn;
struct char_ *pc =
    container_of(&pn->void_, char_, void_);
```

- Translate unions:

```
union { struct char_ c; struct int_ n; } *pu;
char c = pu->c.charM;
                ↓
struct void_ *pu;
char c = container_of(pu, char_, void_)->charM;
```

Polymorphic separation analysis

Regions

Definition

Regions are sets of (structurally distinct) typed pointer expressions s. t. any pair of pointer expressions taken from different regions necessarily addresses disjoint memory areas (w.r.t the corresponding pointer types)

$$\forall r_1, r_2 \in \mathbb{R}.$$

$$r_1 \neq r_2 \implies$$

$$\forall p_1 \in r_1, p_2 \in r_2.$$

$$((\text{char } *)p_1) + (0 \dots \text{sizeof}(p_1)) \cap$$

$$((\text{char } *)p_2) + (0 \dots \text{sizeof}(p_2)) = \emptyset$$

Polymorphic separation analysis

Regions (identifiers of the expression sets)

r	::=	region
	v	variable or fresh block – root region
	$f_p(r)$	dereference region
	$f_s(r)$	nested region
	$f_s^{-1}(r)$	outer region

Congruence closure of a relation \sim (least fix point)

$$\begin{array}{ll} \text{REFL} & \frac{}{r \sim r} \\ & \text{SYMM} \quad \frac{r_1 \sim r_2}{r_2 \sim r_1} \\ & \text{TRANS} \quad \frac{\begin{array}{c} r_1 \sim r_2 \\ r_2 \sim r_3 \end{array}}{r_1 \sim r_3} \\ \text{CONG} & \frac{r_1 \sim r_2}{f(r_1) \sim f(r_2)} \quad f \in \{f_p, f_s, f_s^{-1}\} \\ & \text{INV} \quad \frac{f_s(r) \sim r_1}{f_s^{-1}(r_1) \sim r} \end{array}$$

Roots and kinds

$$root(v) = v$$
$$root(f(r)) = root(r), f \in \{f_p, f_s, f_s^{-1}\}$$
$$kind(r) = \begin{cases} \text{GLOBAL}, & \exists r'. r \sim r' \wedge root(r') \text{ is a global variable} \\ \text{POLY}, & \text{otherwise, if } \exists r'. r \sim r' \wedge root(r') \text{ is a parameter} \\ \text{LOCAL}, & \text{otherwise} \end{cases}$$

Polymorphic separation analysis

Substitutions

A function call $f(t_{p_1}, \dots, t_{p_k}, t_{n_1}, \dots, t_{n_l})$, where f has parameters $p_1, \dots, p_k, p_{k+1}, \dots, p_{k+l}$ determines a substitution σ_f , such that $\sigma_f(p_1) = t_{p_1}, \dots, \sigma_f(p_k) = t_{p_k}, \sigma_f(p_{k+1}) = t_{n_1}, \sigma_f(p_{k+l}) = t_{n_l}$.

Substitutions on regions

$$\begin{aligned}\sigma(v) &= \begin{cases} \sigma(p_i), & v \text{ is a parameter } p_i \\ v, & v \text{ is a global variable} \end{cases} \\ \sigma(f(r)) &= f(\sigma(r)), f \in \{f_p, f_s, f_s^{-1}\}\end{aligned}$$

Projection of a congruence relation

$$\sim' = \{(r_1, r_2) | r_1 \sim r_2, \text{kind}(r_i) \in \{\text{GLOBAL}, \text{POLY}\}, i = 1, 2\}$$

Substitutions on congruence relations

$$\sigma(\sim_f) = \left\{ (\sigma(r_1), \sigma(r_2)) \mid r_1 \sim'_f r_2 \right\}$$

Polymorphic separation analysis

Regions of pointer terms

$$\begin{aligned} r(p) &= p \\ r(\text{NULL}) = r(\star) &= v^*, \text{where } v^* \text{ is a fresh dummy variable name} \\ r(t_p + t_n) &= r(t_p) \\ r(t_p \rightarrow f_p) &= f_p(r(t_p)) \\ r(\&t_p \rightarrow f_s) &= f_s(r(t_p)) \\ r(\text{container_of}(t_p, f_s)) &= f_s^{-1}(r(t_p)) \end{aligned}$$

Separation analysis on instructions

$$\begin{aligned} t_{n1} = t_{n2} &\quad \text{---} \\ t_{p1} = t_{p2} &\quad \sim \leftarrow CC\left(\sim \cup \{(r(t_{p1}), r(t_{p2}))\}\right) \\ \text{assert}(a(t_n, \dots, t_n)) &\quad \text{---} \\ \text{assume}(a(t_n, \dots, t_n)) &\quad \text{---} \\ f(t_p, \dots, t_p, t_n, \dots, t_n) &\quad \sim \leftarrow CC\left(\sim \cup \sigma_f(\sim_f)\right), \\ \text{where } CC(R) &\text{ is congruence closure of the relation } R \end{aligned}$$

Modularity of polymorphic separation analysis

Flow-sensitive analyses

```
// a ↦ a, b ↦ b, c ↦ c ,  
// a->f ↦ d, c->f ↦ e  
a->f = b;  
// a ↦ a, b ↦ b, c ↦ c ,  
// a->f ↦ b, c->f ↦ e
```

```
// a ↦ a, b ↦ b, c ↦ a ,  
// a->f ↦ d, c->f ↦ d  
a->f = b;  
// a ↦ a, b ↦ b, c ↦ a ,  
// a->f ↦ b, c->f ↦ b
```

- How to **quickly** compute results for $a \sim c$ based on the results for $a \approx c$?
- No general solution for control-flow sensitive analyses ...

Flow-insensitive analysis

```
// f(a) = b  
a->f = b;  
// f(a) = b
```

```
// f(a) = b ∧ a = c  
a->f = b;  
// f(a) = b ∧ a = c
```

- The summary \sim is a conjunction of equality of constraints **closed under congruence**
- $f(a) = b \wedge a = c \implies f(c) = b$ by congruence closure
- There are **fast** and **incremental** congruence closure algorithms, e.g., [R. Nieuwenhuis and A. Oliveras "Proof-producing congruence closure", 2005], so just combine the constraints using the algorithm!

Polymorphic separation analysis

Reduced sample program

```
1 struct pair { int a, b; };
2 struct s { struct pair *p;
3             int v };
4 struct sP { struct s* sM; };
5 void swap(struct sP *x,
6           struct sP *y)
7 {
8     struct s *t = x->sM;
9     x->sM = y->sM;
10    y->sM = t;
11    struct pair *p = t->p;
12    t->p = x->sM->p;
13    x->sM->p = p;
14 }
```

```
15 void caller()
16 {
17     struct s *x = alloca(...),
18         *y = alloca(...),
19         *z = alloca(...);
20     struct sP *px = alloca(...),
21         *py = alloca(...),
22         *pz = alloca(...);
23     px->sM = x;
24     py->sM = y;
25     pz->sM = z;
26     // ...
27     swap(px, py);
28     swap(py, pz);
29 }
```

Polymorphic separation analysis

Sample program. Result of separation analysis

$x = \cdot, sM(x) = \cdot, y = \cdot, p(sM(x)) = \cdot; z = \cdot, px = \cdot, \dots, p(z) = \cdot$

```
1 struct pair { int a, b; };
2 struct s { struct pair *p;
3             int v };
4 struct sP { struct s* sM; };
5 void swap(struct sP *x,
6           struct sP *y)
7 {
8     struct s *t = x->sM;
9     x->sM = y->sM;
10    y->sM = t;
11    struct pair *p = t->p;
12    t->p = x->sM->p;
13    x->sM->p = p;
14 }
```

```
15 void caller()
16 {
17     struct s *x = alloca(...),
18     *y = alloca(...),
19     *z = alloca(...);
20     struct sP *px = alloca(...),
21     *py = alloca(...),
22     *pz = alloca(...);
23     px->sM = x;
24     py->sM = y;
25     pz->sM = z;
26     // ...
27     swap(px, py);
28     //  $\sigma_{swap@27} = \{ \cdot \mapsto \cdot, \cdot \mapsto \cdot, \cdot \mapsto \cdot, \cdot \mapsto \cdot \}$ 
29     swap(py, pz);
30     //  $\sigma_{swap@29} = \{ \cdot \mapsto \cdot, \cdot \mapsto \cdot, \cdot \mapsto \cdot, \cdot \mapsto \cdot \}$ 
31 }
```

Dependence set inference

Control flow graph

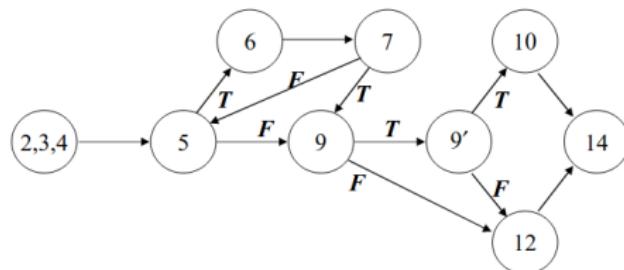
Consider control flow graph, where **statements** are represented by **vertices** and **states** (control flow transitions) are represented by **edges**. This is different from what is used in CPACHECKER, but more convenient for the following definition

Dependence

Consider an **if** statement s with condition $a(t_{n1}, \dots, t_{nk})$. Assume its **post-dominator** closest to s is the statement s_d . Then any statement s' different from both s and s_d on any path starting from s and finishing in s_d is called **dependent from** the condition $a(t_{n1}, \dots, t_{nk})$. We write $a(t_{n1}, \dots, t_{nk}) \in d(s')$

Dependence sets

Thus every statement s can be ascribed the corresponding dependence set $d(s)$ of conditions $a(t_{n1}, \dots, t_{nk})$



For $s = 5$, $s_d = 9$.
So $a_5 \in d(6)$, $a_5 \in d(7)$.
For $s = 9$, $s_d = 14$.
So $a_9 \in d(9')$, $a_9 \in d(10)$, $a_9 \in d(12)$.
E.g. $d(10) = \{a_9, a_{9'}\}$

Effect inference

Access effects $e(t)$

Access effect is a set of variables (v or p),

fields accesses (pairs of the form (r, f_n) or (r, f_p)) and a special non-deterministic effect *

$$\begin{array}{lll} e(v) & = & \{v\} \\ e(a(t_{n1}, \dots, t_{nk})) & = & \bigcup_{i=1}^k e(t_{ni}) \\ e(*) & = & \{*\} \\ e(t_{p_1} - t_{p_2}) & = & e(t_{p_1}) \cup e(t_{p_2}) \\ e(t_p \rightarrow f_n) & = & e(t_p) \cup (r(t_p), f_n) \\ e(container_of(t_p, f_s)) & = & e(t_p) \end{array} \quad \begin{array}{lll} e(p) & = & \{p\} \\ e(NULL) & = & \emptyset \\ e(\star) & = & \{*\} \\ e(t_p + t_n) & = & e(t_p) \cup e(t_n) \\ e(t_p \rightarrow f_p) & = & e(t_p) \cup (r(t_p), f_p) \\ e(\&t_p \rightarrow f_s) & = & e(t_p) \end{array}$$

Assignment effects $A(i)$

Assignment effects are sets of ordered pairs, where elements of pairs are access effects

$$\begin{array}{ll} A(\underbrace{t_1 = t_2}_i) & = \{(e_1, e_2) \mid e_1 \in e(t_1), e_2 \in e(t_2) \cup e(d(i))\} \\ A(\underbrace{assert(a(t_{n1}, \dots, t_{nk}))}_i) & = \emptyset \\ A(\underbrace{assume(a(t_{n1}, \dots, t_{nk}))}_i) & = \left\{ (e_1, e_2) \mid \begin{array}{l} e_1 \in e(t_{ni}), e_2 \in e(t_{nj}) \cup e(d(i)), \\ i, j \in \{1, \dots, k\} \end{array} \right\} \\ A(\underbrace{f(t_p, \dots, t_p, t_n, \dots, t_n)}_i) & = \left\{ (e_1, e_2) \mid \begin{array}{l} \exists e'_2. (e_1, e'_2) \in \sigma_f(A_f^*), \\ (e_1, e_2) \in \sigma_f(A_f^*) \vee e_2 \in e(d(i)) \end{array} \right\} \end{array}$$

Effect inference

Access effects $e(t)$

Access effect is a set of variables (v or p),

fields accesses (pairs of the form (r, f_n) or (r, f_p)) and a special non-deterministic effect *

$$\begin{array}{llll} e(v) & = & \{v\} & e(p) = \{p\} \\ e(a(t_{n1}, \dots, t_{nk})) & = & \bigcup_{i=1}^k e(t_{ni}) & e(\text{NULL}) = \emptyset \\ e(*) & = & \{*\} & e(\star) = \{*\} \\ e(t_{p1} - t_{p2}) & = & e(t_{p1}) \cup e(t_{p2}) & e(t_p + t_n) = e(t_p) \cup e(t_n) \\ e(t_p \rightarrow f_n) & = & e(t_p) \cup (r(t_p), f_n) & e(t_p \rightarrow f_p) = e(t_p) \cup (r(t_p), f_p) \\ & & & e(\&t_p \rightarrow f_s) = e(t_p) \\ e(\text{container_of}(t_p, f_s)) & = & e(t_p) & \end{array}$$

Dependency effects $D(i)$

$$\begin{array}{ll} D(t_1 = t_2) & = \emptyset \\ D\left(\underbrace{\text{assert}\left(a(t_{n1}, \dots, t_{nk})\right)}_i\right) & = e\left(a(t_{n1}, \dots, t_{nk})\right) \cup e(d(i)) \\ D\left(\text{assume}\left(a(t_{n1}, \dots, t_{nk})\right)\right) & = \emptyset \\ D\left(\underbrace{f(t_p, \dots, t_p, t_n, \dots, t_n)}_i\right) & = \sigma_f(D_f^*) \cup e(d(i)) \end{array}$$

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Slicing

$$A = \bigcup_i A(i), \quad D = \bigcup_i D(i)$$

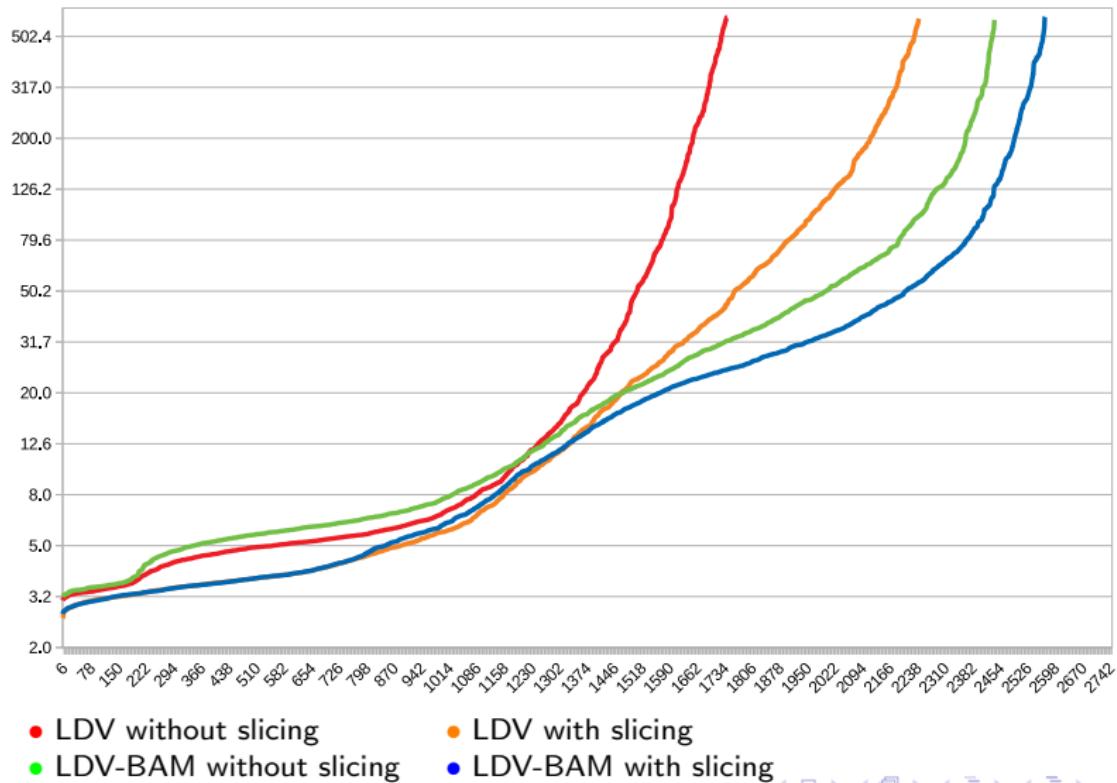
A^* is the transitive closure of A ,
 D^* is the closure of D under A^*

Slicing criterion

The instruction is **retained** iff $\text{dom } A(i) \cap D^* \neq \emptyset$

Results

SoftwareSystems from SV-COMP'18, CPAchecker LDV and LDV-BAM,
400s for slicing, 600s in total, 8GB RAM



- LDV without slicing
- LDV with slicing
- LDV-BAM without slicing
- LDV-BAM with slicing

Strengthening other analyses

Other analyses e.g. predicate and explicit-value can be **strengthened** with region separation to improve precision in presence of **unknown pointers**. When assignment through an unknown pointer is encountered, the destination memory area can be **over-approximated by region**

Scope-bounded verification

- Over-approximate deeper functions with their stubs — non-deterministic **effects** (A^* and D^*) computed by the separation analysis
- Inline function bodies when a **counterexample** containing stubs is discovered
- Preliminary results show that additional summaries (e.g. symbolic execution) are needed to achieve significant speedups
- There are ideas on function call **releancy detection** based on effects and path formulas

Thank you!

Contacts

ISP RAS

Mikhail Mandrykin

email: mandrykin@ispras.ru